

**AMENDMENTS TO THE ABSTRACT**

Please substitute the following paragraph(s) for the abstract now appearing in the currently filed specification:

--There is provided an information processing method characterized in that, in accordance with an instruction from a host CPU 411, either a CPU 103 or 104 loads a common code and an instruction code defined to be executed by itself from an external memory 110 into an internal memory 101, the other CPU loads an instruction code defined to be executed by itself from the external memory 110 into the internal memory 101, and then the respective CPUs load the respective instruction codes defined to be executed by themselves that are loaded in the internal memory 101, and execute the common code that is loaded in the internal memory 101 as required.--